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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/691,115	10/21/2003	Paul Andrys	SK00002C1 (00CXT0656C1)	5326
34408	7590	06/19/2006	EXAMINER	
THE ECLIPSE GROUP 10605 BALBOA BLVD., SUITE 300 GRANADA HILLS, CA 91344			NGUYEN, KHANH V	
			ART UNIT	PAPER NUMBER
			2817	

DATE MAILED: 06/19/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application N . 10/691,115	Applicant(s) ANDRYS ET AL.	
	Examiner Khanh V. Nguyen	Art Unit 2817	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 03 April 2006.
2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-27 is/are pending in the application.
4a) Of the above claim(s) _____ is/are withdrawn from consideration.
5) ☐ Claim(s) _____ is/are allowed.
6) ☒ Claim(s) 1-4, 8-14 and 18-27 is/are rejected.
7) ☒ Claim(s) 5-7 and 15-17 is/are objected to.
8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 112

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 10, 20, 22, 24, 25, 27 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claims 10, 20, 25, 27, it is not clear if "a transistor" claimed is an additional transistor or transistor already disclosed in the associated independent claims 1, 11, 21.

Claim 22, the step of "determining if the bias current is above a predetermined threshold" is not clearly describe in the specification.

Claim 24, it is not clear if "the bias current I_{ref} " is the actual "a bias current" claimed in claim 21.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1, 3, 4, 8, 9, 11, 13, 14, 18, 19, 21 are rejected under 35 U.S.C. 102(e) as being anticipated by Sowlati et al. (6,300,837).

Regarding claims 1, 11, 21, Sowlati et al. (Fig. 2) disclose a power amplifier circuit comprising: an at least one resistor (128); a capacitor (134) coupled to the at least one resistor (128); a bias boosting voltage can be read as a bias voltage for generating a bias input terminal connected to the resistor (128); and an at least one transistor (123) connected to the at least one resistor (128) by an electrical path (line connected one end of resistor (128) to the base of transistor (123)) resulting in as bias current when the bias voltage is applied.

Regarding claims 3, 13, wherein a linear relationship between the bias current and the bias voltage should be inherently seen since they are corresponding to one another.

Regarding claims 4, 14, wherein components (124, 130, 132) can be read as a circuit claimed.

Regarding claims 8, 18, wherein Fig. 2 discloses a single stage amplifier (100).

Regarding claims 9, 19, wherein Fig. 3 discloses a multi-stage amplifier (142, 100).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the

invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

Claims 2, 12, 23 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sowlati et al.

Sowlati et al. disclose the claimed invention except a clamp circuit having the connection/function thereof. However, it is well known in the art in order to limit current flow in a circuit, a clamp circuit should be utilized, wherein clamp circuit is usually a diode(s)/diode connected transistors. Therefore, providing a clamp circuit in the electrical path claimed would have been obvious to a person having ordinary skill in the art.

Allowable Subject Matter

Claims 5-7, 10, 15-17, 20 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claim 10 would be allowable if rewritten to overcome the rejection(s) under 35 U.S.C. 112, 2nd paragraph, set forth in this Office action and to include all of the limitations of the base claim and any intervening claims.

Claims 5-7, 15-17 call for, among others, wherein the at least one resistor is in a first material in a substrate and at least one component of the plurality of components is a second material in the substrate and different from the first material.

Claims 10, 20 call for, among others, a feedback loop and reference current is mirrored from the bias current.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. The additional references (Denning et al. (6,313,705); Wake et al. (6,150,649)) show further analogous prior art circuitry having bias voltage supply and clamping means.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Khanh V. Nguyen whose telephone number is (571) 272-1767. The examiner can normally be reached from 8:00 AM - 4:30 PM EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on (571) 272-1769. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9306 for regular communications and (703) 872-9306 for After Final communications.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



KHANH VAN NGUYEN
PRIMARY EXAMINER
Art Unit: 2817